



SEMICONDUCTOR PINOUTS

FOR NEO-400, SAME AS GBE-400 EXCEPT FOR POWER TRANSFORMER

POWER AMPLIFIER PCB

37
38
39
40

2/20/01: BEGIN D
01/22/02: CHANGE

R39=22K (WAS 15K). R42=680K).

BK (WAS 3.9K) 57M 2017. 01/01/11

[illegible]

7A171 7A171C

D: 27 (D1 NOT USED)

LED: 0

11/15/02: CHANGE
04/24/03: MODIFY

2.2K (WAS 1.8K) TO CALIBRATE TC
Y PER CHANGES 7 LAYER/ (AND 7

SAMPLE	DATE	TIME	ANALYST	INSTRUMENT	REMARKS
1AFLU1	24	15:30			

SCHEMATIC DIAGRAM

[illegible]

VR: 2
Q: 34

U.S. 0

04/23/03: CHANG
05/19/03: ADD D2
05/22/03:

PACKAGE OF Q33. RECALIBRATE SETPOINT

S 2.2k)

DATE DRAWN: 02/20/01 (A.F.)

00112674

FWB: —

FWB: 1

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DATE RECEIVED: 01/12/06 (AE/EA)

RAY (400) 245 2

1000000

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